

Notice of References Cited	Application/Control No. 10/631,205	Applicant(s)/Patent Under Reexamination CHAUVEL ET AL.	
	Examiner Jared I. Rutz	Art Unit 2187	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Lopriore, Lanfranco, Line fetch/Prefetch in a stack cache memory, November 1993, Microprocessors and Microsystems, Volume 17, Number 9, pages 547-555
	V	Jim Handy, The Cache Memory Book, 1998, Academic Press, Second Edition, pages 7, 51-57, and 65-66
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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